

## Assoc. Prof. CAN BÜLENT FİDAN

### Personal Information

**Birthyear:** 1966

**Place of birth:** Olacak / Edirne

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### Education Information

Doctorate, Yildiz Technical University, Faculty Of Electrical & Electronics, Electrical Engineering Department, Turkey 1993 - 2001

Post Graduate, Yildiz Technical University, Faculty Of Electrical & Electronics, Electronics And Communication Engineering, Turkey 1989 - 1991

Under Graduate, Yildiz Technical University, Faculty Of Electrical & Electronics, Electronics And Communication Engineering, Turkey 1983 - 1988

### Dissertations

Doctorate, Dirsek Üstü Kol Protezinin Y.S.A. Kullanılarak DSP Tabanlı Bir Devre İle Gerçek Zamanda Kontrolü, Yildiz Technical University, Graduate School Of Natural And Applied Sciences, Elektrik Mühendisliği (Dr), 2001

Post Graduate, Mikrodenetleyici Destekli Klavye Kartı -Oparatris Cihazı-, Yildiz Technical University, Faculty Of Electrical & Electronics, Electronics And Communication Engineering, 1990

### Research Areas

Electrical and Electronics Engineering, Electronic Circuits, Engineering and Technology

### Academic Titles / Tasks

Associate Professor, Karabuk University, Mühendislik Fakültesi, Mekatronik Mühendisliği, 2020 - Continues

Assistant Professor, Karabuk University, Mühendislik Fakültesi, Mekatronik Mühendisliği, 2011 - 2020

Assistant Professor, Kırklareli Üniversitesi, Teknik Eğitim Fakültesi, Elektrik Eğitimi Bölümü, 2009 - 2011

Lecturer PhD, Qafqaz University, Mühendislik Fakültesi, Bilgisayar Mühendisliği, 2007 - 2008

Lecturer PhD, Yildiz Technical University, Faculty Of Electrical & Electronics, Electronics And Communication Engineering, 2006 - 2007

Research Assistant, Yildiz Technical University, Faculty Of Electrical & Electronics, Electronics And Communication Engineering, 1993 - 2006

### Professional Experience

Vice Dean, Kırklareli Üniversitesi, Teknik Eğitim Fakültesi, Elektrik Eğitimi Bölümü, 2010 - 2011

Head of Department, Kırklareli Üniversitesi, Teknik Eğitim Fakültesi, Elektrik Eğitimi Bölümü, 2009 - 2011

Head of Department, Kırklareli Üniversitesi, Teknik Eğitim Fakültesi, Elektrik Eğitimi Bölümü, 2009 - 2011

## Advising Theses

Fidan C. B. , Robotlarla nesne tanıma, Post Graduate, Y.ÇAPKAN(Student), 2018

Fidan C. B. , Durağan hal görsel uyarılmış potansiyel temelli beyin bilgisayar arayüzü tasarımı ve uygulaması, Doctorate, A.TALHA(Student), 2017

Fidan C. B. , Kaos tabanlı çift entropi çekirdekli gerçek rasgele sayı üretici tasarımı ve FPGA üzerinde gerçekleştirilmesi, Doctorate, M.TUNA(Student), 2017

Fidan C. B. , Lityum tabanlı batarya paketleri için batarya yönetim sistemi tasarımı, Post Graduate, Ş.SOYDAŞ(Student), 2015

Fidan C. B. , EMG sinyalleri kullanarak model bir araba kontrolü için parametre analizi, Post Graduate, A.AKAY(Student), 2015

## Articles Published in Journals That Entered SCI, SSCI and AHCI Indexes

- I. **Design, FPGA implementation and statistical analysis of chaos-ring based dual entropy core true random number generator**  
Koyuncu İ., Tuna M., Pehlivan İ., FİDAN C. B. , Alçın M.  
Analog Integrated Circuits and Signal Processing, vol.102, pp.445-456, 2020 (Journal Indexed in SCI Expanded)
- II. **High speed FPGA-based chaotic oscillator design**  
TUNA M., Alcin M., KOYUNCU İ., FİDAN C. B. , PEHLİVAN İ.  
MICROPROCESSORS AND MICROSYSTEMS, vol.66, pp.72-80, 2019 (Journal Indexed in SCI)
- III. **Novel spatial filter for SSVEP-based BCI: A generated reference filter approach**  
SÖZER A. T. , FİDAN C. B.  
COMPUTERS IN BIOLOGY AND MEDICINE, vol.96, pp.98-105, 2018 (Journal Indexed in SCI)
- IV. **A Study on the importance of chaotic oscillators based on FPGA for true random number generating (TRNG) and chaotic systems**  
TUNA M., FİDAN C. B.  
JOURNAL OF THE FACULTY OF ENGINEERING AND ARCHITECTURE OF GAZI UNIVERSITY, vol.33, pp.469-486, 2018 (Journal Indexed in SCI)
- V. **Electronic circuit design, implementation and FPGA-based realization of a new 3D chaotic system with single equilibrium point**  
TUNA M., FİDAN C. B.  
OPTİK, vol.127, pp.11786-11799, 2016 (Journal Indexed in SCI)
- VI. **Effective and Reliable Speed Control of Permanent Magnet DC (PMDC) Motor under Variable Loads**  
TUNA M., FİDAN C. B. , KOCABEY S., GÖRGÜLÜ S.  
JOURNAL OF ELECTRICAL ENGINEERING & TECHNOLOGY, vol.10, pp.2170-2178, 2015 (Journal Indexed in SCI)
- VII. **Environmental assessment of wind energy conversion systems in Turkey**  
Kocabey S., Nogay S., Fidan C. B. , Dursun B., Gorgulu S., Gokcol C., Akinci T. C. , Aykut E.  
Journal of Environmental Protection and Ecology, vol.13, pp.307-318, 2012 (Journal Indexed in SCI Expanded)
- VIII. **Treatment of skewed multi-dimensional training data to facilitate the task of engineering neural models**  
Altun H., Bilgil A., Fidan B.  
Expert Systems with Applications, vol.33, pp.978-983, 2007 (Journal Indexed in SCI)
- IX. **Treatment of multi-dimensional data to enhance neural network estimators in regression problems**  
Altun H., Bilgil A., Fidan B.

## Articles Published in Other Journals

- I. **Emotiv Epoc ile Durağan Hal Görsel Uyarılmış Potansiyel Temelli Beyin Bilgisayar Arayüzü Uygulaması**  
SÖZER A. T. , FİDAN C. B.  
Bitlis Eren Üniversitesi Fen Bilimleri Dergisi, vol.8, pp.158-166, 2019 (Other Refereed National Journals)
- II. **Novel Detection Features for SSVEP Based BCI: Coefficient of Variation and Variation Speed**  
SÖZER A. T. , FİDAN C. B.  
BRAIN-BROAD RESEARCH IN ARTIFICIAL INTELLIGENCE AND NEUROSCIENCE, vol.8, pp.144-150, 2017 (Journal Indexed in ESCI)
- III. **Comparison of edge detectors on FPGA SoC system**  
SEZER S., FİDAN C. B.  
American Academic Scholarly Research Journal, vol.8, 2016 (Refereed Journals of Other Institutions)

## Books & Book Chapters

- I. **The Chaos-Based Dual Entropy Core TRNG On FPGA: VHDL CODES of Chaotic Systems**  
TUNA M., FİDAN C. B. , KOYUNCU İ.  
LAP LAMBERT Academic Publication, 2019

## Refereed Congress / Symposium Publications in Proceedings

- I. **4 Eksenli Robot Kolun Kinematik Hesaplarının Gerçek Model Üzerinde Matlab ile Analizi ve Testi**  
FİDAN C. B.  
2020 International Congress on Human-Computer Interaction, Optimization and Robotic Applications (HORA), Ankara, Turkey, Turkey, 26 - 27 June 2020
- II. **EMG SİNYALLERİNİN ELDE EDİLMESİ VE SINIFLANDIRILMASI İÇİN FPGA KARTI İLE DEVRETASARIMI**  
FİDAN C. B.  
MAS 4. ULUSLARARASI MATEMATİK-MÜHENDİSLİK-FEN VE SAĞLIK BİLİMLERİ KONGRESİ, Antalya, Turkey, 21 - 23 March 2019, pp.1-12
- III. **FPGA-Based Design of a Novel TRNG**  
TUNA M., FİDAN C. B. , KOYUNCU İ., PEHLİVAN İ.  
International Advanced Researches Engineering Congress-2017, 16 - 18 November 2017
- IV. **FPGA-based Real time Implementation of Lü-Chen Chaotic Generator**  
KOYUNCU İ., TUNA M., FİDAN C. B. , PEHLİVAN İ.  
International Advanced Researches Engineering Congress-2017, 16 - 18 November 2017
- V. **KAOTİK OSİLATÖR TABANLI GÖMÜLÜ GERÇEK RASGELE SAYI ÜRETECİ**  
TUNA M., FİDAN C. B. , KOYUNCU İ., PEHLİVAN İ.  
Uluslararası Mühendislik Araştırmaları Sempozyum Programı, Düzce, Turkey, 11 - 13 September 2017
- VI. **EFFECTS OF THE COUNT OF HIDDEN NEURONS ON ARTIFICIAL NEURAL NETWORKS**  
SEZER S., FİDAN C. B.  
International Science and Technology Conference -istec2016, VİYENA, Austria, 13 - 15 July 2016, pp.420
- VII. **FPGA Tabanlı IQ-Math Formatına Uygun Yüksek Hızlı Kaotik Osilatör Tasarım**  
TUNA M., FİDAN C. B. , KOYUNCU İ., PEHLİVAN İ.  
4TH INTERNATIONAL SYMPOSIUM ON INNOVATIVE TECHNOLOGIES IN ENGINEERING AND SCIENCE, ALANYA, Turkey, 3 - 05 November 2016, pp.354-363

- VIII. **Real Time Hardware Implementation of the 3D Chaotic Oscillator which having Golden-Section Equilibra**  
TUNA M., FİDAN C. B. , Koyuncu I., PEHLİVAN İ.  
24th Signal Processing and Communication Application Conference (SIU), Zonguldak, Turkey, 16 - 19 May 2016, pp.1309-1312
- IX. **Implementation of a Steady State Visual Evoked Potential Based Brain Computer Interface**  
SÖZER A. T. , FİDAN C. B.  
24th Signal Processing and Communication Application Conference (SIU), Zonguldak, Turkey, 16 - 19 May 2016, pp.1173-1176
- X. **Real Time Implementation of A Novel Chaotic Generator on FPGA**  
TUNA M., Koyuncu I., FİDAN C. B. , PEHLİVAN İ.  
23rd Signal Processing and Communications Applications Conference (SIU), Malatya, Turkey, 16 - 19 May 2015, pp.698-701
- XI. **Duragan Hal Görsel Uyarılmış Potansiyel Temelli Beyin Bilgisayar Arayüzleri**  
SÖZER A. T. , FİDAN C. B.  
ELECO 2014, Bursa, Turkey, 27 - 29 November 2014, pp.636-640
- XII. **Durağan Hal Görsel Uyarılmış Potansiyel Temelli Beyin Bilgisayar Arayüzleri**  
SÖZER A. T. , FİDAN C. B.  
Eleco 2014, Turkey, 27 - 29 November 2014
- XIII. **Sıfır Gerilm Anahtarlama Düşük Güçlü İndüksiyon Isıtıcı Araştırma ve Uygulaması**  
SOYDAŞ Ş., FİDAN C. B.  
TOK 2014, Kocaeli, Turkey, 11 - 13 September 2014, pp.963-966
- XIV. **LİTYUM POLİMER BATARYALAR İÇİN BATARYA YÖNETİM SİSTEMİ ARAŞTIRMA VE UYGULAMASI**  
SOYDAŞ Ş., FİDAN C. B.  
TOK 2014, Kocaeli, Turkey, 11 - 13 September 2014, pp.967-972
- XV. **EMG İşaretlerini Sınıflamada Kullanılan İşaret İşleme Tekniklerinin Karşılaştırılması**  
KARLIK B., KOÇYİĞİT Y., FİDAN C. B.  
Proceedings of the IEEE 13th Signal Processing and Communications Applications Conference, 2005., Kayseri, Turkey, 16 - 18 May 2005

## Supported Projects

FİDAN C. B. , Project Supported by Higher Education Institutions, DURAĞAN HAL GÖRSEL UYARILMIŞ POTANSİYEL TEMELLİ BEYİN BİLGİSAYAR ARAYÜZÜ TASARIMI VE UYGULAMASI, 2015 - 2018

FİDAN C. B. , Project Supported by Higher Education Institutions, A NOVEL SPATIAL FILTER METHOD FOR SSVEP BASED BCI: PARTICLE SWARM OPTIMIZATION FOR GENERATING OPTIMUM REFERENCE SIGNAL, 2016 - 2017

## Scientific Refereeing

IEEE TRANSACTIONS ON INDUSTRIAL ELECTRONICS, SCI Journal, March 2002

## Citations

Total Citations (WOS):87

h-index (WOS):5